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# UTILITY PATENT APPLICATION TRANSMITTAL

(Only for new nonprovisional applications under 37 CFR 1.53(b))

Attorney Docket No.

00CXT03401

First Named Inventor or Application Identifier

Shahriar Najand

Title

DIGITAL IMAGE CROSS TALK CORRECTION

Express Mail Label No.

EL 704727800 US

## APPLICATION ELEMENTS

See MPEP Chapter 600 concerning utility patent application contents

## ADDRESS TO:

Assistant Commissioner for Patents  
Box Patent Application  
Washington, DC 20231

1. ☒ Fee Transmittal Form (e.g., PTO/SB/17)  
(Submit an original, and a duplicate for fee processing)
2. ☒ Specification [Total Pages **33**]  
(preferred arrangement set forth below)  
- Descriptive title of the invention  
- Cross References to Related Applications  
- Statement Regarding Fed sponsored R&D  
- Reference to Microfiche Appendix  
- Background of the invention  
- Brief Summary of the invention  
- Brief Description of the Drawings (if filed)  
- Detailed Description  
- Claim(s) - INCLUDED  
- Abstract of the Disclosure - INCLUDED
3. ☒ Drawing(s) (35 USC d113) [Total Sheets **11**]
4. ☒ Oath or Declaration (including Supplemental Declaration) [Total Pages **4**]  
a. ☒ Executed (original or copy)  
b. ☐ Copy from a prior application (37 CFR §1.63(d))  
(for continuation/divisional with Box 17 completed)  
[Note Box 5 below]  
i. ☐ DELETION OF INVENTOR(S)  
Signed statement attached deleting inventor(s)  
named in the prior application,  
see 37 CFR§1.63(d)(2) and 1.33(b)
5. ☐ Incorporation By Reference (useable if Box 4b is checked)  
The entire disclosure of the prior application, from which a copy of  
the oath or declaration is supplied under Box 4b, is considered as  
being part of the disclosure of the accompanying application and is  
hereby incorporated by reference therein.

6. ☐ Microfiche Computer Program (Appendix)
7. ☐ Nucleotide and/or Amino Acid Sequence Submission  
(if applicable, all necessary)  
a. ☐ Computer Readable Copy  
b. ☐ Paper Copy (identical to computer copy)  
c. ☐ Statement verifying identical of above copies

## ACCOMPANYING APPLICATION PARTS

8. ☒ Assignment Papers (cover sheet & Documents(s))
9. ☐ 37 CFR §3.73(b) Statement (when there is an assignee) ☒ Power of Attorney
10. ☐ English Translation Document (if applicable)
11. ☐ Information Disclosure Statement (IDS)/PTO-1449 ☐ Copies of IDS Citations
12. ☐ Preliminary Amendment
13. ☒ Return Receipt Postcard (MPEP 503)  
(Should be specifically itemized)
14. ☐ Small Entity Statement(s) ☐ Statement filed in prior application  
Status still proper and desired  
(PTO/SB/09-12)
15. ☐ Certified Copy of Priority Document(s)  
if foreign priority is claimed
16. ☒ Other A check in the amount of \$730 00.

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17. If a CONTINUING APPLICATION, check appropriate box and supply the requisite information below and in a preliminary amendment:

☐ Conversion ☐ Divisional ☐ Continuation-in-part (CIP) of provisional application No:

Prior application information: Anticipated Examiner:

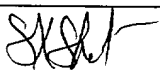
## 18. CORRESPONDENCE ADDRESS

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**FEE TRANSMITTAL**

Patent fees are subject to annual revision on October 1.

These are the fees effective October 1, 1997

Small Entity payments must be supported by a small entity statement, otherwise large entity fees must be paid. See Forms PTO/SB/09-12**Complete If Known**

Application Number	Unassigned
Filing Date	September 29, 2000
First Named Inventor	Shahriar Najand
Examiner Name	Unassigned
Group / Art Unit	Unassigned
Attorney Docket No.	00CXT03401

TOTAL AMOUNT OF PAYMENT (\$ ) 730.00

**METHOD OF PAYMENT**

- 1.
- ☒
- The Commissioner is hereby authorized to charge to the following Deposit Account,

Deposit Account Number

01-0660

Deposit Account Name

Akin Gump Strauss Hauer &amp; Feld

☒ Charge any additional fee required or credit any overpayment☐ Charge all indicated fees and any additional fee required or credit any overpayment

- 2.
- ☒
- Payment Enclosed:**



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**FEE CALCULATION****1. BASIC FILING FEE**

Large Fee Code	Entity Fee (\$)	Small Fee Code	Entity Fee (\$)	Fee Description	Fee Paid
101	760	201	380	Utility filing fee	\$690.00
106	310	206	155	Design filing fee	\$
107	480	207	240	Plant filing fee	\$
108	760	208	380	Reissue filing fee	\$
114	150	214	75	Provisional filing fee	\$
<b>SUBTOTAL (1)</b>					<b>(\$690.00)</b>

**2. EXTRA CLAIM FEES**

Total Claims	Extra Claims	Fee from below	Fee Paid
20	-20** = 0	18	0
3	-3** = 0	78	0
Multiple Dependent		0	0

\*\*or number previously paid, if greater; For Reissue, see below

103	18	203	9	Claims in excess of 20
102	78	202	39	Independent Claims in excess of 3
104	260	204	130	Multiple dependent claims in excess of 3
109	78	209	39	**Reissue independent claims over original patent
110	18	210	9	**Reissue claims in excess of 20 and over original patent
<b>SUBTOTAL (2)</b> (\$ )				

**FEE CALCULATION (continued)****3. ADDITIONAL FEES**

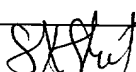
Large Fee Code	Entity Fee (\$)	Small Fee Code	Entity Fee (\$)	Fee Description	Fee Paid
105	130	205	65	Surcharge - late filing fee	
127	50	227	25	Surcharge - late provisional filing fee or cover sheet	
139	130	139	130	Non-English specification	
147	2,520	147	2,520	For filing a request for reexamination	
112	920*	112	920*	Requesting publication of SIR prior to Examiner action	
113	1,840*	113	1,840*	Requesting publication of SIR after Examiner action	
115	110	215	55	Extension for reply within first month	
116	400	216	200	Extension of time within second month	
117	950	217	475	Extension of time within third month	
118	1,510	218	755	Extension of time within fourth month	
128	2,060	228	1,030	Extension of time within fifth month	
119	310	219	155	Notice of Appeal	
120	310	220	155	Filing a brief in support of an appeal	
121	270	221	135	Request for oral hearing	
138	1,510	138	1,510	Petition to institute a public use proceeding	
140	110	240	55	Petition to revive - unavoidable	
141	1,320	241	660	Petition to revive - unintentional	
142	1,320	242	660	Utility issue fee (or reissue)	
143	450	243	225	Design issue fee	
144	670	244	335	Plant issue fee	
122	130	122	130	Petitions to the Commissioner	
123	50	123	50	Petitions related to provisional applications	
126	240	126	240	Submission of Information Disclosure Stmt.	
581	40	581	40	Recording each patent assignment per property (time number of properties)	40.00
146	790	246	395	Filing a submission after final rejection (37 CFR 1.129(a))	
149	790	249	395	For each additional invention to be	
Other fee (specify)					
Other fee (specify)					
<b>*Reduced by Basic Filing Fee Paid</b>					
<b>SUBTOTAL (3)</b>					<b>(\$40.00)</b>

SUBMITTED BY  
Typed or Printed Name

Shayne X. Short, Ph.D.

Date : September 29, 2000

Signature



Reg. Number 45,105

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## DIGITAL IMAGE CROSS TALK CORRECTION

5

### BACKGROUND OF THE INVENTION

#### 1. Technical Field.

10 The invention relates to image processing and, more particularly, to cross talk correction within digital image processing schemes.

#### 2. Related Art.

15 Many conventional methods that are geared to perform cross talk correction within digital image processing schemes commonly employ a single pass method over the entire digital image that inherently does not provide a high degree of accuracy. The cross talk correction often does converge in a highly accurate manner, given the use of only a single iteration of cross talk correction. Those conventional methods that seek to employ multi-pass cross talk correction commonly employ a frame buffer for intermediate data storage. This manner of performing cross talk correction in a multi-pass technique is radically expensive in terms of memory requirements.

20 That is to say, the entire image needs to be stored as an intermediary to perform a multi-pass technique. For example, a single image can contain over 1 million bytes of memory for a SXGA image. These processing requirements commonly lead design engineers to generate cross talk correction methods that provide less than full correction. Given the enormous memory requirements, designers are often bound to solutions that do not provide full cross talk correction,

at least not to convergence of whatever correction method is employed, within image data processing systems that do not have extremely large hardware budget.

In addition to those methods that inherently require enormous memory requirements, other conventional methods have sought to perform cross talk correction in a predetermined sequence. For example, in an RGB (red, green, blue) image, the red pixels are corrected first, the green pixels are corrected next, and finally the blue pixels are corrected. However, some of the deficiencies include significant irregularities and uneven distribution over all of the pixel colors within the image. A large amount of image processing is performed on some of the pixels within the digital image, while little to no image processing is performed on other pixels within the same digital image. These irregularities commonly result in high cost and difficulty in implementation in hardware. The amount of redesigning and debugging required to accommodate all of the various and different digital image types may be enormous. For example, to accommodate one digital image type, the designer must specifically design a method adaptable to that image type. Similarly, to accommodate another digital image type, the designer must specifically design another method adaptable to that image type. There simply lacks the ability to adapt such a conventional method universally to different types of digital images.

For a clearer understanding of the problem associated with cross talk, the following illustration is provided showing the diffusion of light from one pixel into its neighbors. Cross talk between neighboring pixels in a digital image occurs when a beam of light aimed for a pixel diffuses into its neighboring pixels and corrupts their values. Using the following image pattern for illustration, when light is aimed at pixel **B1**, a majority of the light aimed at the pixel may in fact captured by the pixel **B1**.

Line 1      R1 G1 R2 G2 ...  
Line 2      G3 **B1** G4 B2 ...  
Line 3      R3 G5 R4 G6 ...  
Line 4      G7 B3 G8 B4 ...  
5            ...            ...

However, some of the light escapes into the neighboring pixels R1, G1, R2, G3, G4, R3, G5, and R4. This undesirable diffusion is color dependent and can be modeled by the following equations for each pixel color:

$$B1m = b1*B1 + b2*G1 + b3*G5 + b4*G3 + b5*G4 + b6*R1 + b7*R2 + b8*R3 + b9*R4$$

$$R4m = r1*R4 + r2*G4 + r3*G5 + r4*G6 + r5*G8 + r6*B1 + r7*B2 + r8*B3 + r9*B4$$

$$G4m = g1*G4 + g2*G1 + g3*G2 + g4*G5 + g5*G6 + g6*B1 + g7*B2 + g8*R2 + g9*R4$$

where the left hand side of the preceding equations are the cross talk corrupted pixel values and the coefficients preceding the pixel values on the right hand side (b1 ... b9, r1 ... r9, and g1 ... g9) represent the diffusion coefficients between the adjacent pixels. The actual values captured by the digital image sensor are corrupted and are represented as follows:

Line 1      R1m G1m R2m G2m ...

Line 2	G3m B1m G4m B2m ...
Line 3	R3m G5m R4m G6m ...
Line 4	G7m B3m G8m B4m ...
...	...

- 5 As mentioned above, there simply does not exist a highly efficient method to perform digital image processing that reduces the undesirable cross talk diffusion between neighboring pixels. The image captured inherently contains the embedded cross talk resulting from the imperfections within the image sensor itself, in that, the pixels of adjacent pixels are not perfectly isolated from one another. Further limitations and disadvantages of conventional and traditional systems will
- 10 become apparent to one of skill in the art through comparison of such systems with the invention as set forth in the remainder of the present application with reference to the drawings.

## SUMMARY

Various aspects of the invention can be found in an image processing system operable to perform cross talk correction to a digital image having a plurality of pixels. The image processing system may include, among other things, an image sensor circuitry, a processing circuitry, and a line buffer circuitry. The image sensor circuitry typically operates to read an image and to generate a digital image. The processing circuitry is communicatively coupled to the image sensor circuitry, and the line buffer circuitry, that itself contains a number of line buffers that are communicatively coupled to the processing circuitry.

The processing circuitry performs cross talk correction on a first pixel contained within a first line of image data. The first line is received from a first line buffer contained within the line buffer circuitry. In addition, the processing circuitry uses a second pixel contained within a second line of image data to perform the cross talk correction on the first pixel. The second line is received from a second line buffer contained within the line buffer circuitry. Also, the processing circuitry receives a third line comprising a third plurality of pixels from the image sensor circuitry.

The processing circuitry may also perform cross talk correction on a third pixel contained within the third line of image data after performing cross talk correction on the first pixel. The processing circuitry uses a fourth pixel contained within a fourth line of image data to perform the cross talk correction on the first pixel to generate the first cross talk corrected pixel, the fourth line is received from a third line buffer contained within the line buffer circuitry. A portion of the second line and a portion of the fourth line are a cross talk correction grid. A predetermined trajectory aligns along the first pixel, the second pixel, and the third pixel within the image data. The processing circuitry is operable to perform multi-pass cross talk correction

on a fourth pixel contained within a fourth line of image data that is received from a fourth line buffer contained within the line buffer circuitry. The first pixel, the second pixel, a third pixel within the third line of image data, and the fourth pixel are aligned along a predetermined trajectory within the digital image. The processing circuitry uses the first cross talk corrected pixel to perform multi-pass cross talk correction on the fourth pixel.

This invention may also apply to image processing systems performing cross talk correction to a digital image having a plurality of pixels. The image processing system may also include, a processing circuitry and a line buffer circuitry. The line buffer circuitry may be communicatively coupled to the processing circuitry. The circuitry performs cross talk correction on a first pixel contained within a first line of image data while the processing circuitry receives a second line of image data. The processing circuitry uses a second pixel contained within the second line of image data to perform the cross talk correction on the first pixel.

The processing circuitry receives a third line comprising a third plurality of pixels from the line buffer circuitry. The line buffer circuitry may include a line buffer, and the second line of image data is contained within the line buffer. The processing circuitry performs cross talk correction on the first pixel contained within the first line of image data while the processing circuitry performs multi-pass cross talk correction on a third pixel contained within a third line of image data while the processing circuitry receives the second line of image data from the line buffer circuitry. The first pixel and the third pixel may be aligned along a predetermined trajectory within the digital image. A portion of the second line and a portion of the fourth line are a cross talk correction grid. The processing circuitry uses the first cross talk corrected pixel to perform multi-pass cross talk correction on the third pixel. The processing circuitry uses a



third pixel contained within a third line of image data to perform the cross talk correction on the first pixel. The line buffer circuitry comprises a first line buffer and a second line buffer, and the second line of image data is contained within the first line buffer, and the third line of image data is contained within the second line buffer.

5 Other aspects of the invention can be found in a method to perform cross talk correction to a digital image. The method includes, among other things, performing cross talk correction on a first pixel contained within a first line of image data to generate a first cross talk corrected pixel, receiving a second line of image data while performing the cross talk correction on the first pixel, and using a second pixel contained within the second line to perform the cross talk  
10 correction on the first pixel.

In certain embodiments of the invention, the method also includes using a third pixel contained within a third line of image data to perform the cross talk correction on the first pixel to generate the first cross talk corrected pixel. A portion of the second line and a portion of the third line are a cross talk correction grid. The method also includes performing multi-pass cross  
15 talk correction on a third pixel contained within a third line of image data using the first cross talk corrected pixel. The method also includes storing the first cross talk corrected pixel in a memory location. Also, the first pixel and the second pixel are aligned along a predetermined trajectory within the digital image.

Other systems, methods, features and advantages of the invention will be or will become  
20 apparent to one with skill in the art upon examination of the following figures and detailed description. It is intended that all such additional systems, methods, features and advantages be included within this description, be within the scope of the invention, and be protected by the accompanying claims.

## BRIEF DESCRIPTION OF THE FIGURES

The components in the figures are not necessarily to scale, emphasis instead being placed upon illustrating the principles of the invention. In the figures, like reference numerals designate corresponding parts throughout the different views.

5           Fig. 1 is a system diagram illustrating an embodiment of an image processing system.

          Fig. 2A is a system diagram illustrating another embodiment of an image processing system at a particular instant in time.

          Fig. 2B is a system diagram illustrating the embodiment of the image processing system shown in Fig. 2A at another instant in time.

10           Fig. 3A is a system diagram illustrating another embodiment of an image processing system at another instant in time.

          Fig. 3B is a system diagram illustrating the embodiment of the image processing system shown in Fig. 3A at another instant in time.

15           Fig. 4A is a digital image diagram illustrating an embodiment of image processing performed along a predetermined trajectory by an image processing system.

          Fig. 4B is a digital image diagram illustrating another embodiment of image processing performed along another predetermined trajectory by an image processing system.

          Fig. 4C is a digital image diagram illustrating another embodiment of image processing performed along yet another predetermined trajectory by an image processing system.

20           Fig. 4D is a digital image diagram illustrating various embodiments of grid sizes that are used to perform cross talk correction, independent of the number of passes performed for cross talk correction.

Fig. 5A is a digital image diagram illustrating a cross section of a digital image on which single pass cross talk correction is performed when a specific pixel within the digital image is read.

5 Fig. 5B is a digital image diagram illustrating the cross section of the digital image on which single pass cross talk correction is performed as shown in the Fig. 5A when a subsequent pixel within the digital image is read.

Fig. 5C is a digital image diagram illustrating a cross section of a digital image on which multi-pass cross talk correction is performed when a specific pixel within the digital image is being read.

10 Fig. 5D is a digital image diagram illustrating the cross section of the digital image on which multi-pass cross talk correction is performed as shown in the Fig. 5C when a subsequent pixel within the digital image is read.

Fig. 6 is a functional block diagram illustrating an embodiment of a cross talk correction method.

## DETAILED DESCRIPTION

Cross talk is an undesirable product of the imperfections in the fabrication of image sensor circuitries. This deleterious effect cannot be completely corrected or removed since a certain degree of information is lost in the image capturing process. Some aspects of this cross talk problem are similar to the problem of de-blurring a low-pass filtered image. However, a very good estimate of the actual image that is being captured is achieved by performing cross talk correction in accordance with the invention. For example, there is virtually no visually perceptual difference between the actual image and the cross talk corrected image resulting from the image processing of the invention.

To calculate, mathematically, the original (uncorrupted) pixels values of a digital image, the diffusion equations for the center pixel value are solved with the following resultant equation. The actual values captured by the digital image sensor are corrupted and are represented as follows:

Line 1	R1m G1m R2m G2m ...
Line 2	G3m B1m G4m B2m ...
Line 3	R3m G5m R4m G6m ...
Line 4	G7m B3m G8m B4m ...
...	...

However, the true value of the pixel B1 is shown below as a function of the measured cross talk corrupted value B1m itself and adjacent pixels.

$$B1 = (B1m - b2*G1 - b3*G5 - b4*G3 - b5*G4 - b6*R1 - b7*R2 - b8*R3 - b9*R4)/b1$$

$$5 \quad B1 = b1m*B1m - b2m*G1 - b3m*G5 - b4m*G3 - b5m*G4 - b6m*R1 - b7m*R2 - b8m*R3 - b9m*R4$$

The actual value of the pixel location B1 is unknown, given the imperfection of the image capture device itself (shown as an image sensor circuitry in the various embodiments of the invention following). However, recursive mathematics are employed to approximate the actual values as shown below. The right hand side of the equation (G1, G5, ...) is unknown, so those values are approximated and replaced with their measured and corrupted values as shown below.

$$15 \quad B1p = b1m*B1m - b2m*G1m - b3m*G5m - b4m*G3m - b5m*G4m - b6m*R1m - b7m*R2m - b8m*R3m - b9m*R4m$$

B1p is partially cross talk corrected and is closer to the real value B1 than B1m is to B1. Continuing on, doing the same thing to all pixels in the image then we get a partially corrected image that is closer to the uncorrupted one. Since all the pixels were processed only once, this particular embodiment illustrates single pass cross talk correction. If desired, all of the partially corrected pixel values are stored in a frame buffer, then the cross talk correction is repeated indefinitely until convergence, or a predetermined number of time, to generate a higher quality

image. However, after several passes the cross talk correction converges and further passes will not improve or modify the image in any visually perceptual manner.

This single pass cross talk correction does not require a frame buffer at all. To correct a pixel, a predetermined number (N) of adjacent rows of the image are used. A number less than the predetermined number (N-1) of lines of the image are saved in line buffers since previous lines of the image are corrected as a current line comes in from an image sensor circuitry. For example, in the instance of using a 3x3 grid size to perform cross talk correction using the single pass solution, the amount of storage is only  $2 \times 1024 = 2048$  pixels -- a reduction in memory storage requirements when compared to conventional solutions that perform cross talk correction.

In addition, multi-pass implementations may be performed where certain pixels along a predetermined trajectory are cross talk corrected more than once during a single pass over the image. For example, in the single pass solution, partially cross talk corrected pixel values are sent to the next block for further processing or stored in a separate storage like a frame buffer in certain embodiments of the invention. However, these same partially cross talk corrected pixels are stored back to the line buffers as they are calculated and are used to perform cross talk correction on subsequent pixels, thereby giving even greater accuracy. This generates the following snapshot of the line buffers as pixel R4 comes in from the sensor when one predetermined trajectory is employed.

Line buffer 1	R1p G1p R2p G2p ...
Line buffer 2	G3p B1m G4m B2m ...

Current Line from sensor      R3m G5m R4m

R1p ... G3p are the partially corrected values and B1m ... R4m are the uncorrected values. The following equation results for B1.

5

$$B1p = b1*B1m - b2*G1p - b3*G5 - b4*G3p - b5*G4 - b6*R1p - b7*R2p - b8*R3 - b9*R4$$

This above equation is a better partially corrected B1p since other partially corrected neighboring pixels are used in calculating the partially corrected value of B1p. Therefore, this “single pass” method is superior to the single pass solution discussed above, though it is not truly single pass, in that, the pixels in the image are being cross talk corrected multiple times during the one pass over the image. Continuing, by adding another line buffer, and when B4m comes in from the image sensor circuitry, the following snapshot results.

15	Line buffer 1	R1p G1p R2p G2p ...
	Line buffer 2	G3p B1p G4p B2p ...
	Line buffer 3	R3p G5p R4m G6m ...
	Current Line from sensor	G7m B3m G8m B4m

20      At this point, R4m is partially cross talk corrected and stored as R4p in its place so the snapshot will change to the following when the next pixel G9m comes in from the image sensor circuitry.

Line buffer 1	R1p G1p R2p G2p ...
Line buffer 2	G3p B1p G4p B2p ...
Line buffer 3	R3p G5p R4p G6m ...
Current Line from sensor	G7m B3m G8m B4m G9m

5

In this specific embodiment, a complete 3x3 grid of partially corrected pixels (R1p, G1p, R2p, G3p, B1p, G4p, R3p, G5p, R4p) is generated. Furthermore, the accuracy of the value of B1p is increased by applying the cross talk correction equation to it. When this process is continued for all the pixels of the image, then a “second pass” is effectively performed over the entire image. Therefore, by adding a single line buffer to the image processing system, the capability of running a second pass over the image is provided without the use of an expensive frame buffer.

Similarly, additional line buffers are added in certain embodiments of the invention to help perform more “passes” over the image and the cross talk corrected quality of the image is improved even more. This cross talk correction generates high quality image for the same number of passes. It is substantially cheaper than conventional cross talk correction methods in many respects, one being that it uses less memory (e.g., a few line buffers of 1024 pixels each instead of a frame buffer of size 1.3 million pixels). This technique is extendable to grid sizes of any various size. For example, the above illustration shows a 3x3 diffusion model. However, bigger models (5x5 or bigger) are equally operable in accordance with the invention.

Fig. 1 is a system diagram illustrating an embodiment of an image processing system 100 having a processing circuitry 112 and a line buffer circuitry 114. The line buffer circuitry may



have an indefinite number of line buffers illustrated as a line buffer A 116, a line buffer B 117, and a line buffer N 119. Input, cross talk corrupted image data 120 is fed into the image processing circuitry 110. The processing circuitry 112 may be operable to perform cross talk correction using any of the embodiments including single or multi-pass cross talk correction and the use of variable grid sizes for cross talk correction when performing image processing on the cross talk corrupted image data 120. The resultant output from the image processing circuitry 110 is an output, cross talk corrected image data 130. This image data is operable to be stored, transmitted or displayed as will be shown in the various embodiments of the invention below.

Fig. 2A is a system diagram illustrating another embodiment of an image processing system 200 at a particular instant in time. The image processing system 200 may have an image sensor circuitry 250, a line buffer circuitry 214, and a processing circuitry 212. The image sensor circuitry 250 may be operable to read an indefinite number of lines in an image as shown by a line A 251, a line B 252, a line C 253, and a line D 254. The line buffer circuitry 214 itself contains, at least, a line buffer A 216 and a line buffer B 217.

A single line (line C 253 in this Figure) is provided to the processing circuitry 212. This line is presently the raw image data that is read by the image sensor circuitry 250 and itself probably contains cross talk corruption from adjacent pixels. The processing circuitry 212 feeds a previously read line of raw image data into the line buffer B 217. The previously read line of raw image data in this Figure is shown to be the line B 252. A previously read and corrected line of image data is fed into the line buffer A 216. This cross talk corrected line is shown to be the line A 251. In the event that the current line read is the first line of the image (e.g., the line A 251), then the processing circuitry simply loads zeroes into the line buffer A 216. The invention is operable to perform cross talk correction even when there is no data provided from preceding

or adjacent lines and pixels without suffering any catastrophic, operational, or mathematical errors. Boundaries conditions (i.e., edge of an image) are handled identically except zeroes are loaded into the line buffers instead of preceding or adjacent line and pixel values. The processing circuitry 212 is operable to perform mathematical processing on the line B 252 using the cross talk corrected image data from the line A 251. The processing circuitry 212 is operable to perform basic mathematical operations such as multiplication, addition, division, subtraction, as well as linear algebra operations which are used in certain embodiments of the invention when multi-pass cross talk correction and large grid sizes are used as will be shown below.

Fig. 2B is a system diagram illustrating the embodiment of the image processing system 200 shown within the Fig. 2A at another instant in time. Similar to the Fig. 2A, the image processing system 200 may have an image sensor circuitry 250, a line buffer circuitry 214, and a processing circuitry 212. The image sensor circuitry 250 may be operable to read an indefinite number of lines in an image as shown by a line A 251, a line B 252, a line C 253, and a line D 254. The line buffer circuitry 214 itself contains, at least, a line buffer A 216 and a line buffer B 217.

A single line (line D 254 in this Figure) is provided to the processing circuitry 212. This line is presently the raw image data that is read by the image sensor circuitry 250 and itself probably contains cross talk corruption from adjacent pixels. The processing circuitry 212 feeds a previously line of raw image data into the line buffer B 217. The previously read line of raw image data in this Figure is the line C 253 that is read in the Fig. 2A. A previously read and corrected line of image data is fed into the line buffer A 216. This cross talk corrected line is the line B 252 that is corrected in the Fig. 2A. The processing circuitry 212 is operable to perform mathematical processing on the line C 253 using the cross talk corrected image data from the line

B 252 in this single pass embodiment of the invention. The processing circuitry 212 may be operable to perform basic mathematical operations such as multiplication, addition, division, subtraction, as well as linear algebra operations that are used in certain embodiments of the invention when multi-pass cross talk correction and large grid sizes are used.

5 Fig. 3A is a system diagram illustrating another embodiment of an image processing system 300 at another instant in time. The image processing system 300 may have an image sensor circuitry 350, a line buffer circuitry 314, and a processing circuitry 312. The image sensor circuitry 350 is operable to read an indefinite number of lines in an image as shown by a line A 351, a line B 352, ..., a line R 354, a line S 355, a line T 356, a line U 357, ..., and a line Z 359.

10 The line buffer circuitry 314 may also contain an indefinite number of line buffers shown as line buffer A 316, ..., a line buffer N-1 318, ..., and a line buffer N 319.

This line is presently the raw image data that is read by the image sensor circuitry 350 and probably contains cross talk corruption from adjacent pixels. The processing circuitry 312 feeds a previously line of raw image data into the line buffer N 319. A previously read and one time cross talk corrected line of image data in this Figure is shown to be fed into the line buffer N-1 318. Similarly, previously read and N times cross talk corrected line of image data in this Figure is shown to be fed into the line buffer A 316. Any variable grid size is employed to perform the correction of cross talk, as will be shown in even greater detail in the following Figures.

15

The multi-pass cross talk correction is employed such that a one time cross talk corrected line is located in the line buffer N-1 318, and an N times cross talk corrected line is located in the line buffer A 316. The multi-pass solution is operable such that when one pixel is cross talk corrected, additional pixels along a predetermined trajectory within the digital image are also

20

operable to be cross talk corrected as well. These additional pixels along the predetermined trajectory are most likely being cross talk corrected a 2<sup>nd</sup>, 3<sup>rd</sup>, etc. time, i.e., an Nth time.

The processing circuitry 312 is operable to perform mathematical processing on a line contained within the line buffer N 319 in this multi-pass embodiment of the invention. The processing circuitry 312 is operable to perform basic mathematical operations such as multiplication, addition, division, subtraction, as well as linear algebra operations employed to perform multi-pass cross talk correction and variable grid sizes.

Fig. 3B is a system diagram illustrating the embodiment of the image processing system 300 shown in Fig. 3A at another instant in time. Similar to the Fig. 3A, the image processing system 300 may have an image sensor circuitry 350, a line buffer circuitry 314, and a processing circuitry 312. The image sensor circuitry 350 is operable to read an indefinite number of lines in an image as shown by a line A 351, a line B 352, ..., a line R 354, a line S 355, a line T 356, a line U 357, ..., and a line Z 359. The line buffer circuitry 314 may contain an indefinite number of line buffers shown as line buffer A 316, ..., a line buffer N-1 318, ..., and a line buffer N 319.

A single line (now line U 357 in this Figure) is provided to the processing circuitry 312. This line is presently the raw image data that is read by the image sensor circuitry 350 and may contain cross talk corruption from adjacent pixels. The processing circuitry 312 feeds a previously one time cross talk corrected line of raw image data into the line buffer N-1 318. Depending on the grid size, additional one time cross talk corrected line of raw image data into the line buffers adjacent to the line buffer N-1 318. Previously read and N times corrected lines of image data are fed into the line buffer A 316. Similarly, depending on the grid size,

additional N times cross talk corrected lines of raw image data into the line buffers adjacent to the line buffer A 316.

The processing circuitry 312 is operable to perform mathematical processing on a line of the image data in this multi-pass embodiment of the invention. The processing circuitry 312 is operable to perform basic mathematical operations such as multiplication, addition, division, subtraction, as well as linear algebra operations that are used in multi-pass cross talk correction and also when large grid sizes are used to perform cross talk correction.

Fig. 4A is a digital image diagram illustrating an embodiment of image processing performed along a predetermined trajectory by an image processing system 405. An image 407 contains an indefinite number of pixels. The grid sizes employed in the Fig. 4A are exemplary of just one of the many various grid sizes that are each operable to perform cross talk correction and is also shown in Fig. 4D. An IN PIXEL is input from an image sensor circuitry and an X PIXEL (located immediately to the left one pixel and up one pixel from the IN PIXEL) is cross talk corrected. In a single pass solution, the X PIXEL is the last pixel corrected when the IN PIXEL is read. Moreover, when multi-pass solutions are implemented, a Y PIXEL (located immediately to the left one pixel and up one pixel from the X PIXEL) is cross talk corrected at least one time more than the X PIXEL. For example, in the two pass solution, the Y PIXEL is the last pixel corrected when the IN PIXEL is read.

When multi-pass solutions are implemented, a Z PIXEL (located immediately to the left one pixel and up one pixel from the Y PIXEL) is cross talk corrected at least one time more than the Y PIXEL. In a three pass solution, the Z PIXEL is the last pixel corrected when the IN PIXEL is read. Similarly, the invention is indefinitely scaleable to accommodate any number of passes in various multi-pass solutions. When multi-pass solutions are implemented, an N PIXEL

(along the predetermined trajectory defined along the X PIXEL, the Y PIXEL, and the Z PIXEL) is cross talk corrected. In an indefinite N- pass solution, the N PIXEL is the last pixel corrected when the IN PIXEL is read.

The predetermined trajectory may be employed to perform cross talk correction to each of the pixels along the predetermined trajectory. Even in the single pass solution, a predetermined trajectory exists by the choosing of which preceding pixel is going to be cross talk corrected when the IN PIXEL is read. Moreover, different grid sizes are operable while still performing cross talk correction along the predetermined trajectory as shown in the Fig. 4A. For example, each of the X PIXEL, the Y PIXEL, and the Z PIXEL, is a cluster of pixels in certain embodiments of the invention, and a specific pixel within each of the clusters when cross talk is corrected. That is to say, a predetermined number of lines is used for cross talk correction in each of the passes of the multi-pass embodiments of the invention.

Fig. 4B is a digital image diagram illustrating another embodiment of image processing performed along another predetermined trajectory by an image processing system 415. The Fig. 4B contains image 417 with an indefinite number of pixels. Similar to the Fig. 4A, the grid sizes employed in the Fig. 4B are exemplary of just one of the many various grid sizes that are each operable to perform cross talk correction and is shown in Fig. 4D below. An IN PIXEL is input from an image sensor circuitry and an X PIXEL (located to the left two pixels and up one pixel from the IN PIXEL) is cross talk corrected. In a single pass solution, the X PIXEL is the last pixel corrected when the IN PIXEL is read. When multi-pass solutions are implemented, a Y PIXEL (located to the left two pixels and up one pixel from the X PIXEL) is cross talk corrected at least one time more than the X PIXEL. For example, in the two pass solution, the Y PIXEL is the last pixel corrected when the IN PIXEL is read.

When multi-pass solutions are implemented, a Z PIXEL (located to the left two pixels and up one pixel from the Y PIXEL) is cross talk corrected at least one time more than the Y PIXEL. For example, in a three pass solution, the Z PIXEL is the last pixel corrected when the IN PIXEL is read. Similarly, the invention is indefinitely scaleable to accommodate any number of passes in various multi-pass solutions. When multi-pass solutions are implemented, an N PIXEL (along the predetermined trajectory defined along the X PIXEL, the Y PIXEL, and the Z PIXEL) is cross talk corrected. For example, in an indefinite N-pass solution, the N PIXEL is the last pixel corrected when the IN PIXEL is read.

If desired, the predetermined trajectory is also adaptable to be along a trajectory of pixels being located immediately to the left one pixel and up two pixels from a preceding pixel along the predetermined trajectory. These various examples of the predetermined trajectory are included to illustrate the adaptability of the invention to perform cross talk correction of various pixels at various times within the image 417. In addition, similar to the description with respect to the Fig. 4A, different grid sizes are operable while still performing cross talk correction along the predetermined trajectory as shown in Fig. 4B.

Fig. 4C is a digital image diagram illustrating another embodiment of image processing performed along yet another predetermined trajectory by an image processing system 425. The Fig. 4C contains image 427 with an indefinite number of pixels. Similar to the Fig. 4A and the Fig. 4B, the grid sizes employed in the Fig. 4C are exemplary of just one of the many various grid sizes that are each operable to perform cross talk correction and is also shown in Fig. 4D. An IN PIXEL is input from an image sensor circuitry and an X PIXEL (located to the left three pixels and up one pixel from the IN PIXEL) is cross talk corrected. In a single pass solution, the X PIXEL is the last pixel corrected when the IN PIXEL is read. When multi-pass solutions are

implemented, a Y PIXEL (located to the left three pixels and up one pixel from the X PIXEL) is cross talk corrected at least one time more than the X PIXEL. For example, in the two pass solution, the Y PIXEL is the last pixel corrected when the IN PIXEL is read.

When multi-pass solutions are implemented, a Z PIXEL (located to the left three pixels  
5 and up one pixel from the Y PIXEL) is cross talk corrected at least one time more than the Y PIXEL. For example, in a three pass solution, the Z PIXEL is the last pixel corrected when the IN PIXEL is read. Similarly, the invention is indefinitely scaleable to accommodate any number of passes in various multi-pass solutions. When multi-pass solutions are implemented, an N  
10 PIXEL (along the predetermined trajectory defined along the X PIXEL, the Y PIXEL, and the Z PIXEL) is cross talk corrected. For example, in an indefinite N-pass solution, the N PIXEL is the last pixel corrected when the IN PIXEL is read.

If desired, the predetermined trajectory is also adaptable to be along a trajectory of pixels  
being located immediately to the left one pixel and up three pixels from a preceding pixel along  
the predetermined trajectory. These various examples of the predetermined trajectory are  
15 included to illustrate the adaptability of the invention to perform cross talk correction of various pixels at various times within the image 427. In addition, similar to the description of Figs. 4A and 4B, different grid sizes may be operable while still performing cross talk correction along the predetermined trajectory as shown in Fig. 4C.

Fig. 4D is a digital image diagram illustrating various embodiments of grid sizes 435 that  
20 are used to perform cross talk correction, independent of the number of passes performed for cross talk correction. The number of passes used to perform cross talk correction in the various multi-pass embodiments of the invention is independently variable from the number of passes performed. Fig. 4D shows the instances when cross talk is not corrected on a pixel, but rather a



location between pixels. For example, position A, position C, and position E are all exemplary of how it need not be a pixel itself that is being cross talk corrected, but rather a location between pixels.

In the illustration shown in Fig. 4D, position A employs a 2xM grid size to perform cross talk correction. A single line buffer may be required to perform this operation. The grid size 2xM is an indefinitely scaleable rectangle where the height of the rectangle is 2 lines of image data. Similarly, position C employs a 4xM grid size to perform cross talk correction. Three line buffers may be required to perform this operation. The grid size 4xM is an indefinitely scaleable rectangle where the height of the rectangle is 4 lines of image data. Similarly, position E employs a 6xM grid size to perform cross talk correction. Five line buffers may be required to perform this operation. The grid size 6xM is an indefinitely scaleable rectangle where the height of the rectangle may be 6 lines of image data.

For cross talk correction performed on actual pixels within the image, as shown in Fig. 4D, the pixel B employs a 3xM grid size to perform cross talk correction. Two line buffers may be required to perform this operation. The grid size 3xM is an indefinitely scaleable rectangle where the height of the rectangle is 3 lines of image data. Similarly, the pixel D employs a 5xM grid size to perform cross talk correction. Four line buffers may be required to perform this operation. The grid size 5xM may be an indefinitely scaleable rectangle where the height of the rectangle is 5 lines of image data. Similarly, the pixel F employs a 7xM grid size to perform cross talk correction. Six line buffers may be required to perform this operation. The grid size 7xM is an indefinitely scaleable rectangle where the height of the rectangle is 7 lines of image data.

Also shown in Fig. 4D, a pixel or position N employs an NxM grid size to perform cross talk correction. N-1 line buffers are required to perform this operation. The grid size NxM is an indefinitely scaleable rectangle where the height of the rectangle is N lines of image data. Various grid sizes may be operable with various number of passes performed in the multi-pass solutions.

Fig. 5A is a digital image diagram illustrating a cross section snapshot 500 of a digital image on which single pass cross talk correction is performed when a specific pixel within the digital image is read. In Fig. 5A, when the pixel location R4 is read, it is the pixel location G3 (shown as the single time cross talk corrected pixel G3p1) that is cross talk corrected. For example, the measured value (R4m) is read by an image sensor circuitry, and the measured value (G3m) contains some undesirable cross talk corruption, the cross talk is corrected resulting in the cross talk corrected pixel (G3p1).

Fig. 5B is a digital image diagram illustrating the cross section snapshot 502 of the digital image on which single pass cross talk correction is performed as shown in Fig. 5A when a subsequent pixel within the digital image is being read. In Fig. 5B, when the pixel location G6 is read, it is the pixel location B1 (shown as the single time cross talk corrected pixel B1p1) that is cross talk corrected. It is the pixel location G3 (shown as G3p1) that has been cross talk corrected in the previous snapshot as shown in Fig. 5A. For example, the measured value (G6m) is what is actually being read by an image sensor circuitry, and the measured value (B1m) itself contains some undesirable cross talk corruption is cross talk corrected. The pixel location B1, having been cross talk corrected, is shown as (B1p1) indicating a single pass cross talk correction solution.

Fig. 5C is a digital image diagram illustrating a cross section snapshot 504 of a digital image on which multi-pass cross talk correction is performed when a specific pixel within the digital image is read. In Fig. 5C, when the pixel location B4 is read, it is the pixel location G5 (shown as the single time cross talk corrected pixel G5p1) that is cross talk corrected. For example, the measured value (B4m) is read by the image sensor circuitry, and the measured value (G5m) contains some undesirable cross talk corruption that is cross talk corrected to become the cross talk corrected pixel (G5p1).

Fig. 5D is a digital image diagram illustrating the cross section snapshot 506 of the digital image on which multi-pass cross talk correction is performed as shown in Fig. 5C when a subsequent pixel within the digital image is read. In Fig. 5D, when the pixel location G9 is read, it is the pixel location R4 (shown as the single time cross talk corrected pixel R4p1) that is cross talk corrected. It is the pixel location G5 (shown as G5p1) that has been cross talk corrected in the previous snapshot as shown in Fig. 5C. For example, the measured value (G9m) is read by the image sensor circuitry, and the measured value (R4m) contains some undesirable cross talk corruption that is cross talk corrected. The pixel location R4, having been cross talk corrected, is shown as (R4p1) indicating a single pass cross talk correction solution.

The various embodiments of the invention shown in Figures 5A, 5B, 5C, and 5D show one particular predetermined trajectory where cross talk correction is performed. However, those having skill in the art of digital image processing will appreciate that any of the various predetermined trajectories shown in the various Figures are analogously operable without departing from the scope and spirit of the invention. In addition, those predetermined trajectories that are not explicitly illustrated are also within the scope of the invention.

Fig. 6 is a functional block diagram illustrating an embodiment of a cross talk correction method 600. In a block 610, a pixel is received from an image sensor. In a block 620, a predetermined number of pixels are retrieved from a predetermined number of line buffers. In block 630, cross talk correction is performed on a selected pixel or a selected number of pixels that is retrieved from a predetermined number of line buffers in the block 620. The cross talk of the pixels is corrected along a predetermined trajectory as shown in Figures 4A, 4B, and 4C. In addition, the grid size of the cross talk correction is also variable, as shown in Fig. 4D. In a block 640, the predetermined number of cross talk corrected/retrieved pixels are updated and stored into a predetermined number of line buffers. Within the multi-pass embodiments of the invention, certain pixels along the predetermined trajectory are updated 2<sup>nd</sup>, 3<sup>rd</sup>, 4<sup>th</sup>, etc. times, depending on the number of passes employed in the specific multi-pass embodiment. Those updated values are then stored into the predetermined number of line buffers, so that the cross talk corrected values are usable when performing cross talk correction on subsequent pixels. The ability to use partially cross talk corrected values to perform cross talk correction of other pixels, allows for a high degree of accuracy when compared to conventional methods of cross talk correction.

In an alternate block 660, the predetermined number of cross talk corrected/retrieved pixels may be stored, transmitted, and/or displayed. For example, the cross talk correction method 600 does not require that the entire image be stored in any intermediary memory location, as compared with conventional methods, but the cross talk correction method 600 is operable to allow the cross talk corrected/retrieved pixels to be stored in a location for subsequent use. This type of memory is typically cheaper than the dynamic memory required to perform full image data processing used in conventional image processing systems. In addition,

those cross talk corrected/retrieved pixels are operable to be transmitted via local or wide area network. Similarly, the cross talk corrected/retrieved pixels are operable to be displayed using any suitable display system. Finally, in block 650, the current pixel, that is received from the image sensor in the block 610, is stored into a line buffer for subsequent use by other pixels to  
5 perform cross talk correction within the digital image.

While various embodiments of the invention have been described, it will be apparent to those of ordinary skill in the art that many more embodiments and implementations are possible that are within the scope of this invention.

What is claimed is:

- 1 1. An image processing system operable to perform cross talk correction to a digital image  
2 having a plurality of pixels, comprising:  
3 an image sensor circuitry that is operable to read an image to generate a digital image;  
4 a processing circuitry communicatively coupled to the image sensor circuitry;  
5 a line buffer circuitry, communicatively coupled to the processing circuitry, comprising a  
6 plurality of line buffers;  
7 the processing circuitry performs cross talk correction on a first pixel contained within a  
8 first line comprising a first plurality of pixels to generate a first cross talk corrected pixel, the  
9 first line is received from a first line buffer contained within the line buffer circuitry;  
10 the processing circuitry uses a second pixel contained within a second line comprising a  
11 second plurality of pixels to perform the cross talk correction on the first pixel, the second line is  
12 received from a second line buffer contained within the line buffer circuitry; and  
13 the processing circuitry receives a third line comprising a third plurality of pixels from the  
14 image sensor circuitry.
- 1 2. The image processing system of claim 1, wherein the processing circuitry performs cross  
2 talk correction on a third pixel contained within the third line comprising the third plurality of  
3 pixels after performing cross talk correction on the first pixel.
- 1 3. The image processing system of claim 1, wherein the processing circuitry uses a fourth  
2 pixel contained within a fourth line comprising a fourth plurality of pixels to perform the cross

talk correction on the first pixel to generate the first cross talk corrected pixel, the fourth line is received from a third line buffer contained within the line buffer circuitry; and

a subset of the second line and a subset of the fourth line comprise a cross talk correction grid within the plurality of pixels.

4. The image processing system of claim 1, wherein the first pixel, the second pixel and a third pixel within the third plurality of pixels are aligned along a predetermined trajectory within the plurality of pixels of the digital image.

5. The image processing system of claim 1, wherein the processing circuitry performs multi-pass cross talk correction on a fourth pixel contained within a fourth line comprising a fourth plurality of pixels, the fourth line is received from a fourth line buffer contained within the line buffer circuitry.

6. The image processing system of claim 5, wherein the first pixel, the second pixel, a third pixel within the third plurality of pixels, and the fourth pixel are aligned along a predetermined trajectory within the plurality of pixels of the digital image.

7. The image processing system of claim 5, wherein the processing circuitry uses the first cross talk corrected pixel to perform multi-pass cross talk correction on the fourth pixel.

8. An image processing system operable to perform cross talk correction to a digital image having a plurality of pixels, comprising:

a processing circuitry;

a line buffer circuitry communicatively coupled to the processing circuitry;

5 the processing circuitry performs cross talk correction on a first pixel contained within a  
6 first line comprising a first plurality of pixels to generate a first cross talk corrected pixel while  
7 the processing circuitry receives a second line comprising a second plurality of pixels from the  
8 line buffer circuitry; and

9 the processing circuitry uses a second pixel contained within the second line comprising  
10 the second plurality of pixels to perform the cross talk correction on the first pixel.

1 9. The image processing system of claim 8, wherein the processing circuitry receives a third  
2 line comprising a third plurality of pixels from the line buffer circuitry.

1 10. The image processing system of claim 8, wherein the line buffer circuitry comprises a line  
2 buffer; and

3 the second line comprising the second plurality of pixels is contained within the line  
4 buffer.

1 11. The image processing system of claim 8, wherein the processing circuitry performs cross  
2 talk correction on the first pixel contained within the first line comprising the first plurality of  
3 pixels while the processing circuitry performs multi-pass cross talk correction on a third pixel  
4 contained within a third line comprising a third plurality of pixels while the processing circuitry  
5 receives the second line comprising the second plurality of pixels from the line buffer circuitry;  
6 and

7 the first pixel and the third pixel are aligned along a predetermined trajectory within the  
8 plurality of pixels of the digital image.



1 12. The image processing system of claim 11, wherein a subset of the first line and a subset  
2 of the third line comprise a cross talk correction grid within the plurality of pixels.

1 13. The image processing system of claim 11, wherein the processing circuitry uses the first  
2 cross talk corrected pixel to perform multi-pass cross talk correction on the third pixel.

1 14. The image processing system of claim 8, wherein the processing circuitry uses a third  
2 pixel contained within a third line comprising a third plurality of pixels to perform the cross talk  
3 correction on the first pixel.

1 15. The image processing system of claim 14, wherein the line buffer circuitry comprises a  
2 first line buffer and a second line buffer;  
3 the second line comprising the second plurality of pixels is contained within the first line  
4 buffer; and  
5 the third line comprising a third plurality of pixels is contained within the second line  
6 buffer.

1 16. A method to perform cross talk correction to a digital image having a plurality of pixels,  
2 comprising:  
3 performing cross talk correction on a first pixel contained within a first line comprising a  
4 first plurality of pixels to generate a first cross talk corrected pixel;  
5 receiving a second line comprising a second plurality of pixels while performing the cross  
6 talk correction on the first pixel; and

7 using a second pixel contained within the second line comprising the second plurality of  
8 pixels to perform the cross talk correction on the first pixel.

1 17. The method of claim 16, further comprising using a third pixel contained within a third  
2 line comprising a third plurality of pixels to perform the cross talk correction on the first pixel to  
3 generate the first cross talk corrected pixel; and

4 a subset of the second line and a subset of the third line comprise a cross talk correction  
5 grid within the plurality of pixels.

1 18. The method of claim 16, further comprising performing multi-pass cross talk correction  
2 on a third pixel contained within a third line comprising a third plurality of pixels using the first  
3 cross talk corrected pixel.

1 19. The method of claim 16, further comprising storing the first cross talk corrected pixel in a  
2 memory location.

1 20. The method of claim 16, wherein the first pixel and the second pixel are aligned along a  
2 predetermined trajectory within the plurality of pixels of the digital image.

ABSTRACT

A system to perform digital image cross talk correction. The invention provides a solution that provides a radical reduction in the memory and processing system requirements of conventional digital image processing systems. A single line buffer may be used in place of an entire frame buffer in conventional systems. Single or multi-pass implementations of the invention are operable, each implementation being further adaptable to perform cross talk correction using variable grid sizes including any number of desired pixels within the grid. The invention is operable with the only memory requirements being those of line buffers as compared from entire frame buffers employed within conventional cross talk correction image processing solutions. In performing multi-pass cross talk correction, cross talk correction may be achieved on a single pass over the entire image. Certain of the preceding pixels, along a programmed or selected trajectory within the digital image, are cross talk corrected numerous times.

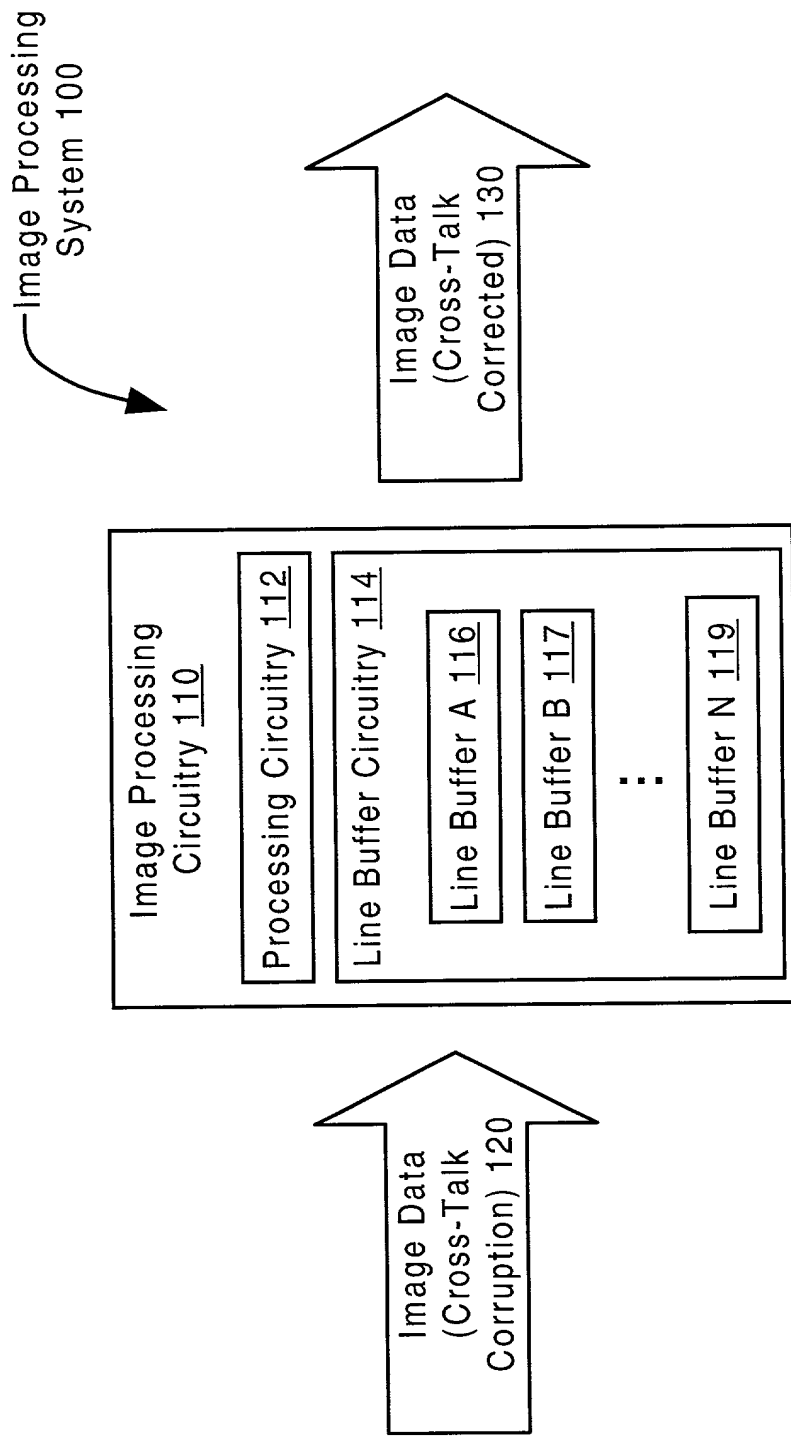


Fig. 1

Image Processing  
System 200

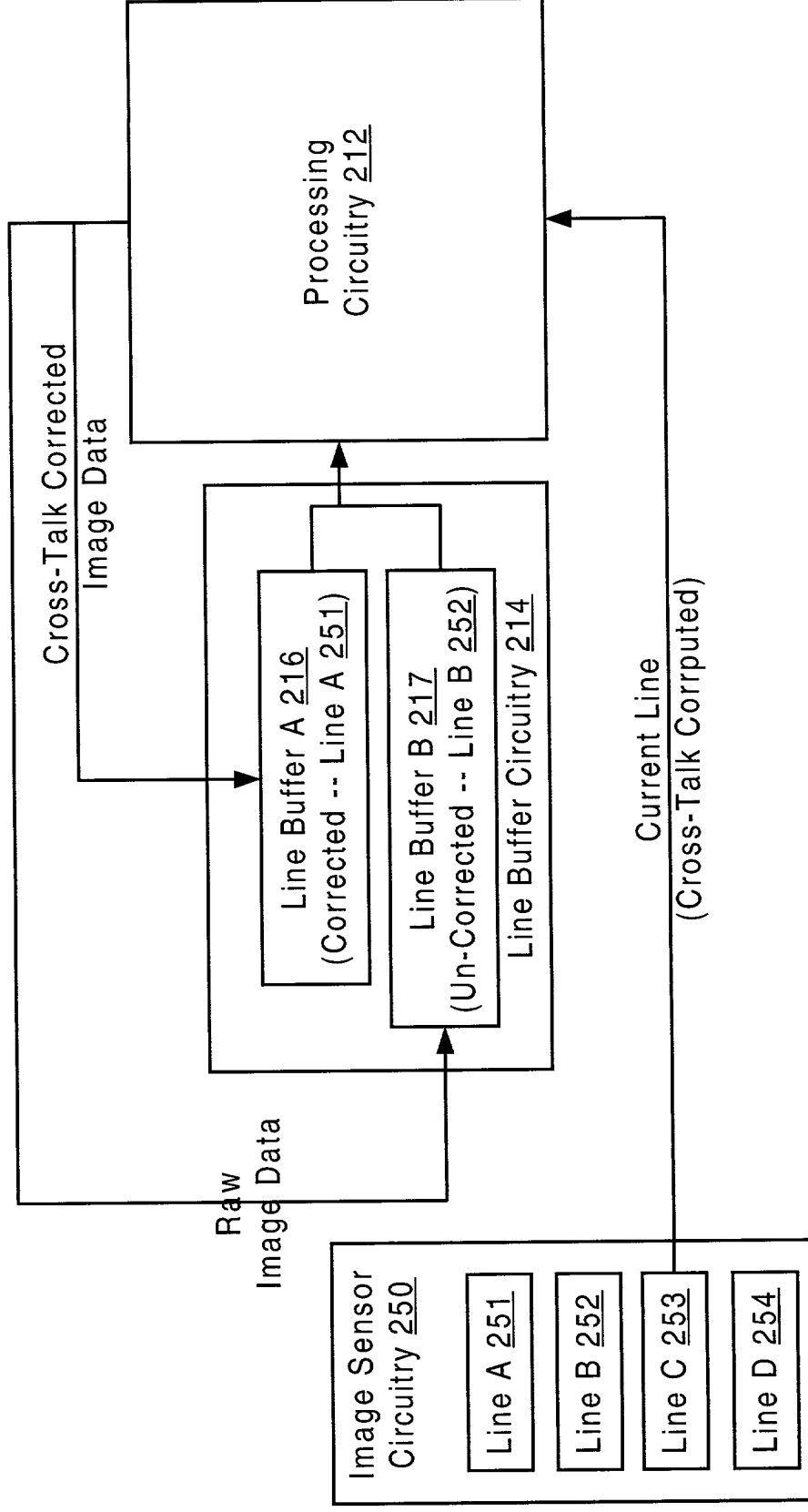


Fig. 2A

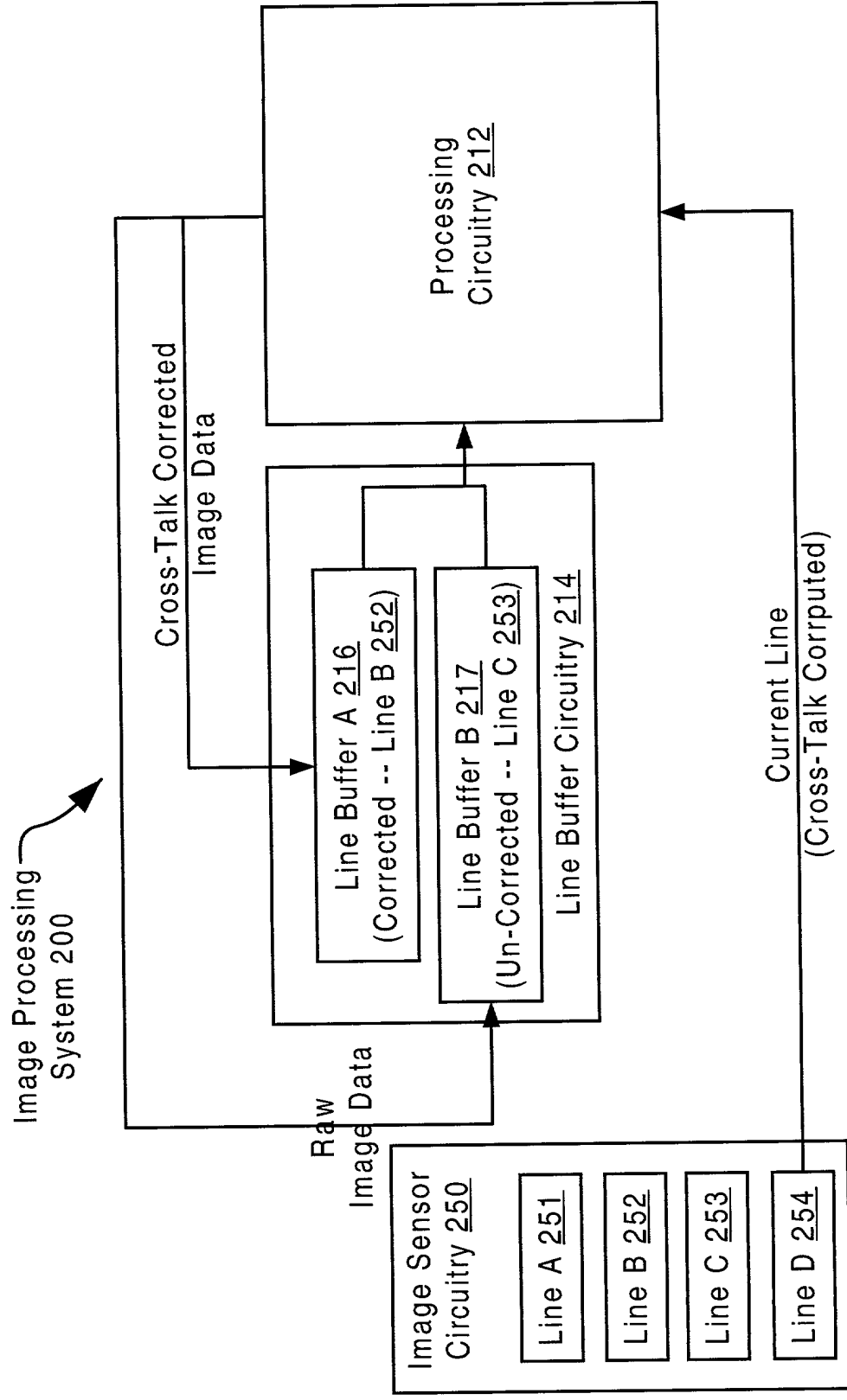


Fig. 2B

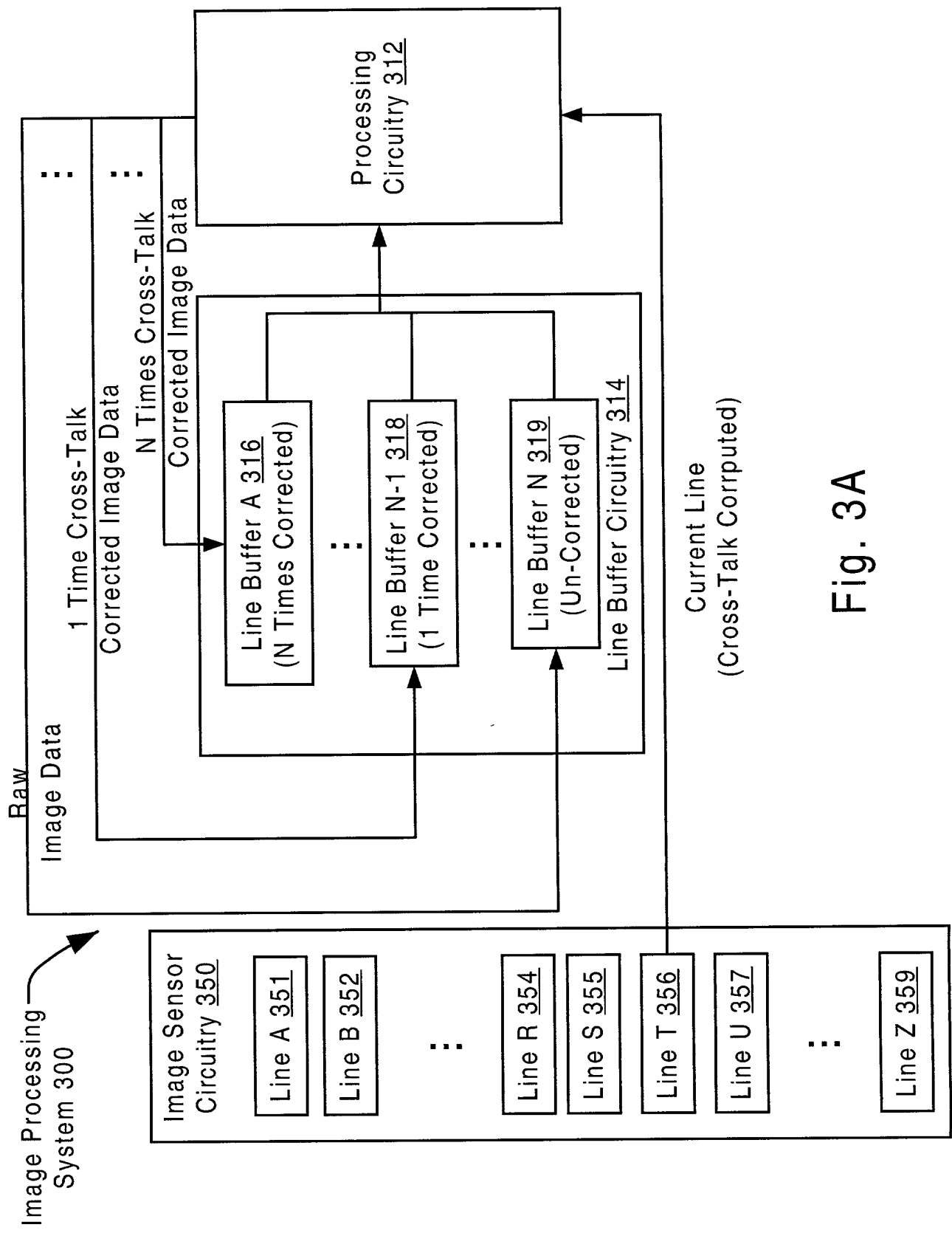


Fig. 3A

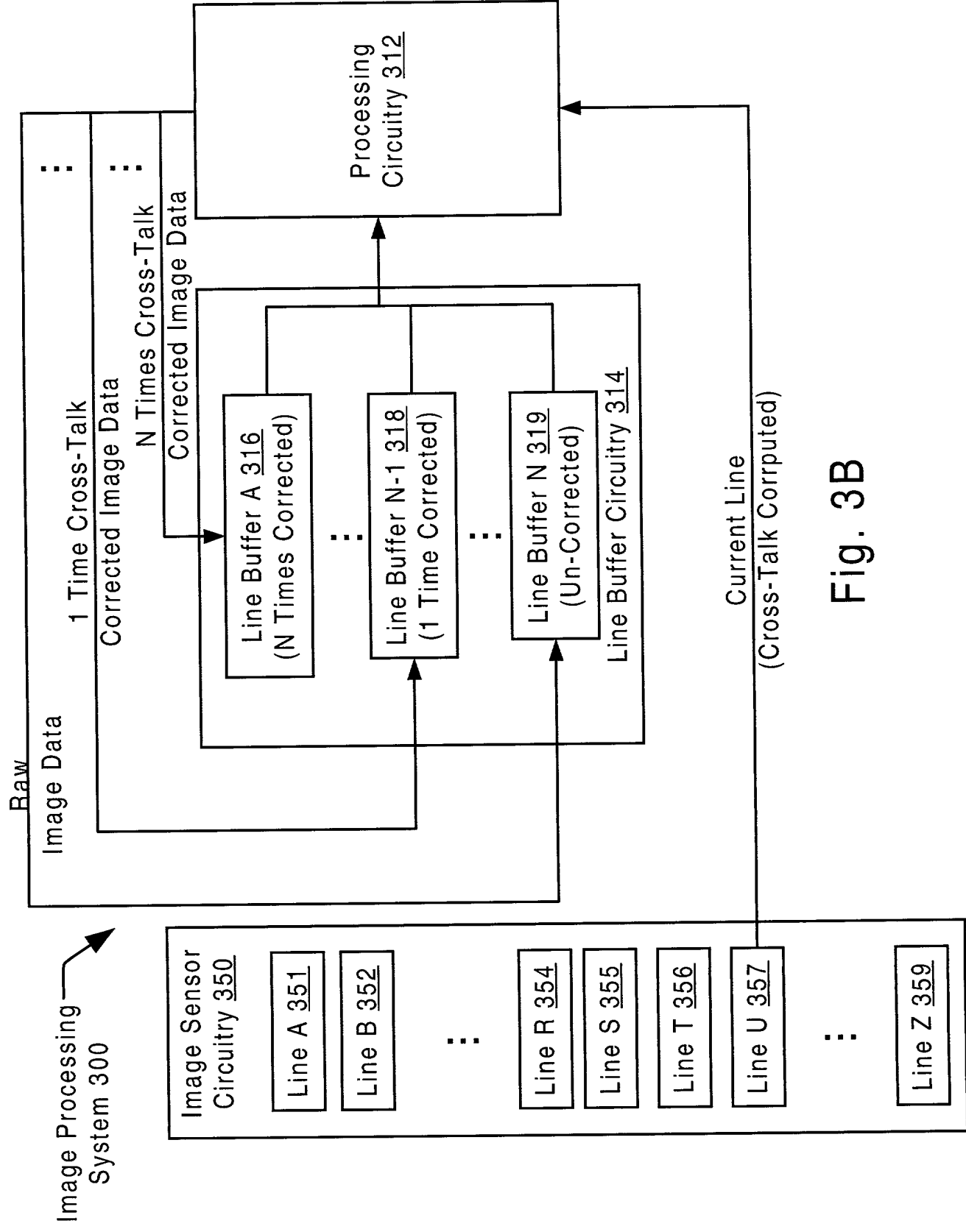




Image Processing  
System 405

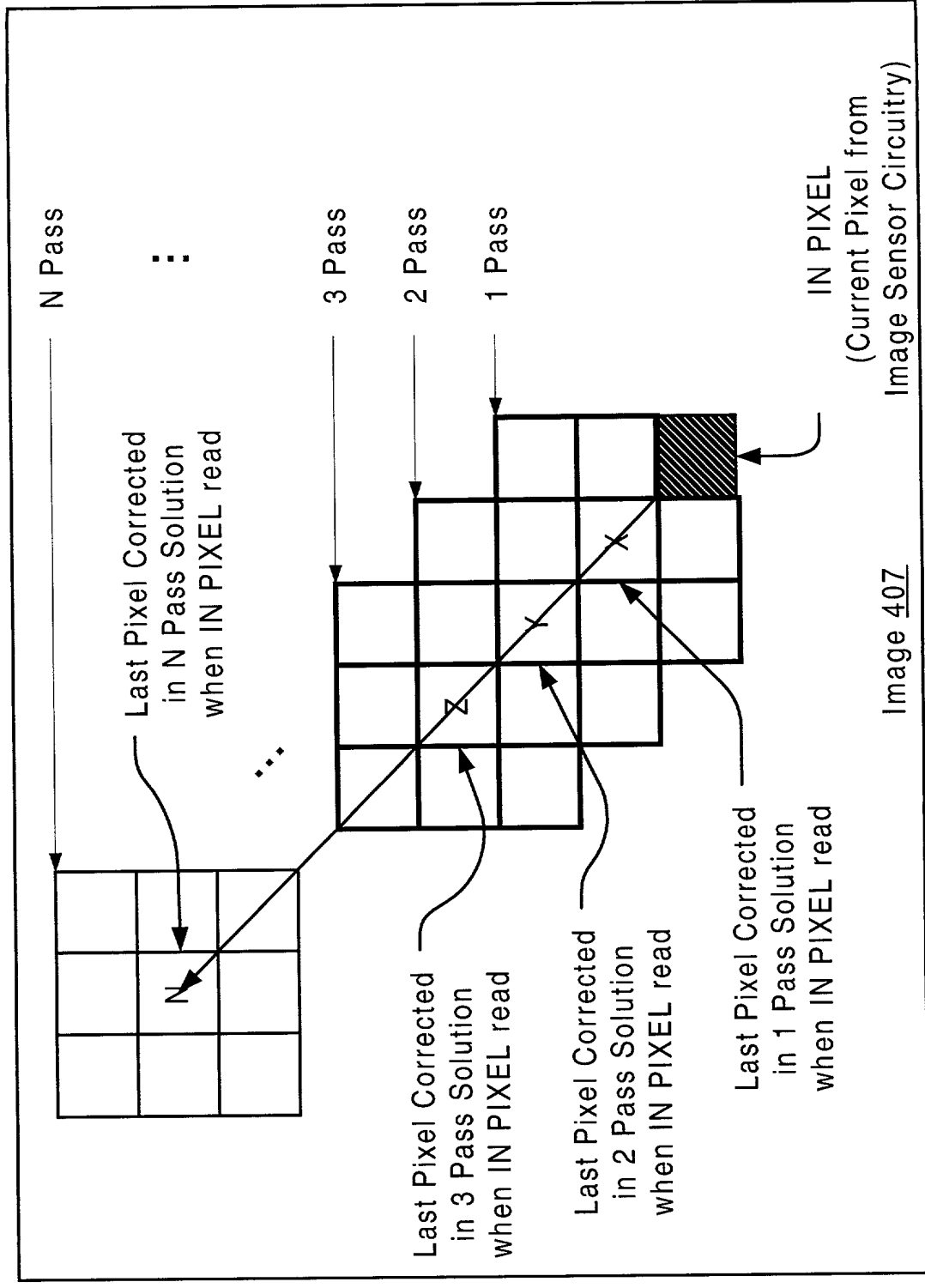


Fig. 4A

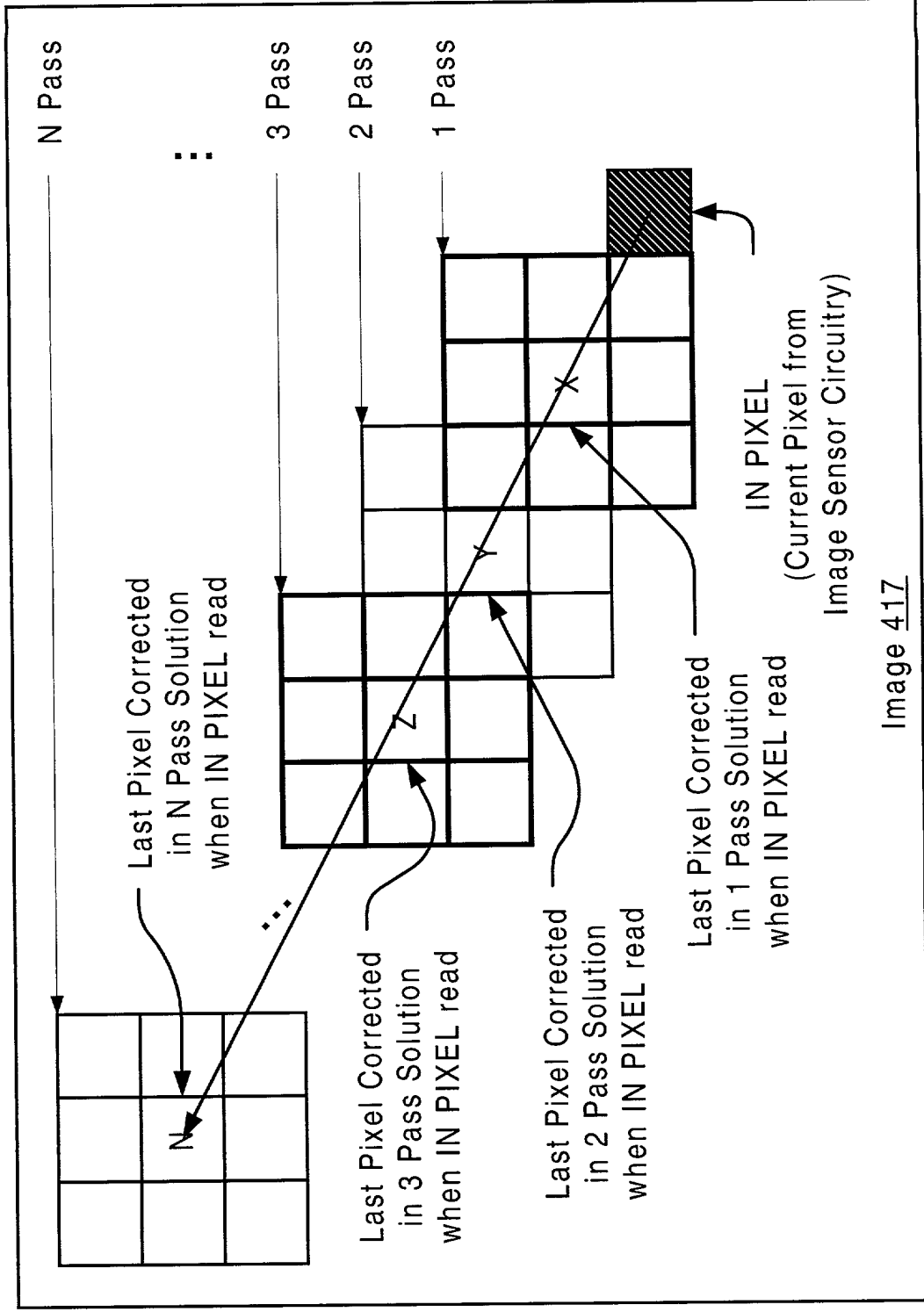


Fig. 4B

Image Processing  
System 425

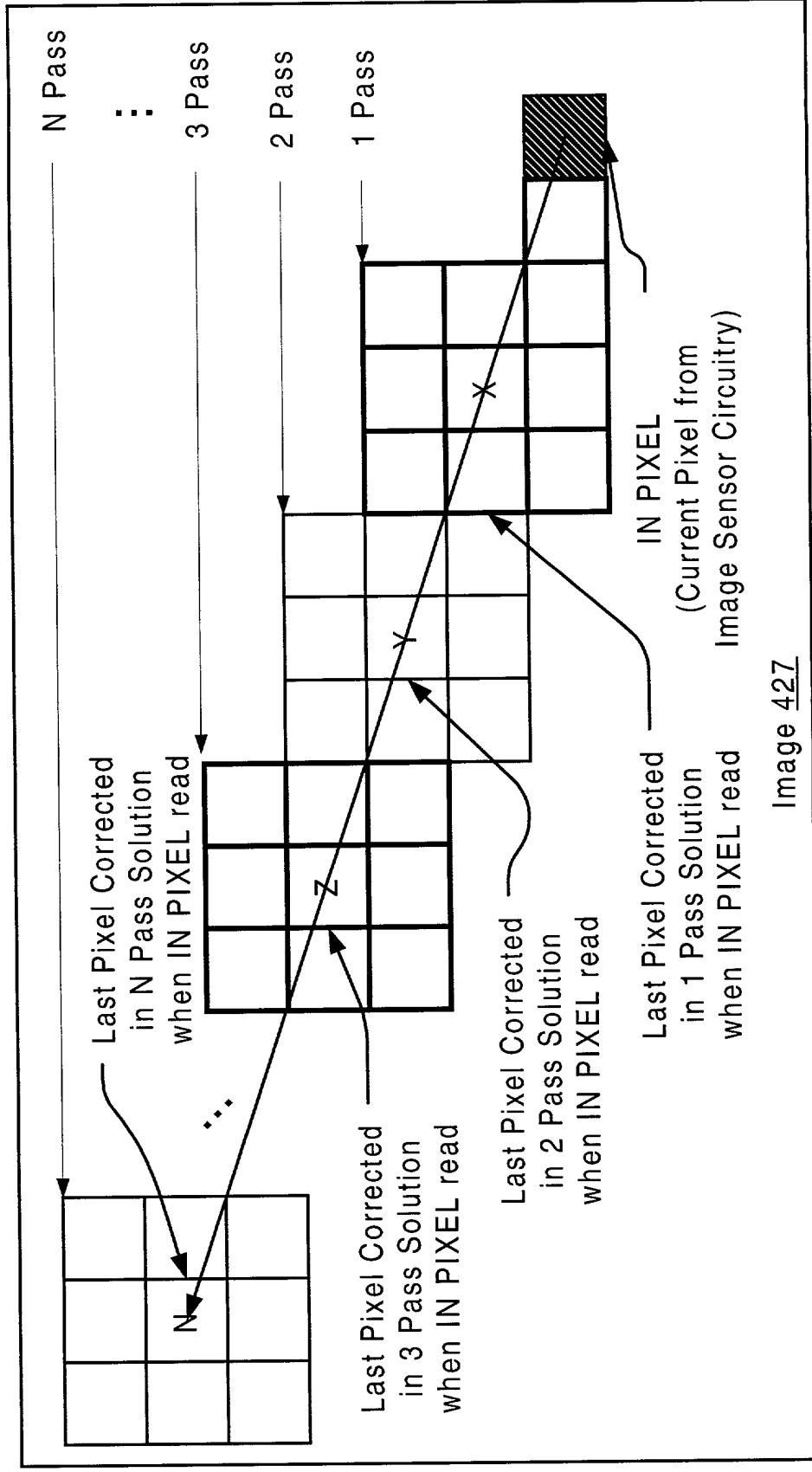


Fig. 4C



R1	G1	R2	G2	...
p1	p1	p1	p1	
G3	B1	G4	B2	...
p1	m	m	m	
R3	G5	R4	...	...
m	m	m		

Fig. 5A

R1	G1	R2	G2	...
p1	p1	p1	p1	
G3	B1	G4	B2	...
p1	p1	m	m	
R3	G5	R4	G6	...
m	m	m	m	

Fig. 5B

Snapshot when R4 read:  
1 Pass Solution  
500

Snapshot when G6 read:  
1 Pass Solution  
502

R1	G1	R2	G2	...
p2	p2	p2	p2	
G3	B1	G4	B2	...
p1	p1	p1	p1	
R3	G5	R4	G6	...
p1	p1	m	m	
G7	B3	G8	B4	...
m	m	m	m	

Fig. 5C

R1	G1	R2	G2	...
p2	p2	p2	p2	
G3	B1	G4	B2	...
p2	p1	p1	p1	
R3	G5	R4	G6	...
p1	p1	p1	m	
G7	B3	G8	B4	G9
m	m	m	m	m

Fig. 5D

Snapshot when B4 read:  
2 Pass Solution  
504

Snapshot when G9 read:  
2 Pass Solution  
506

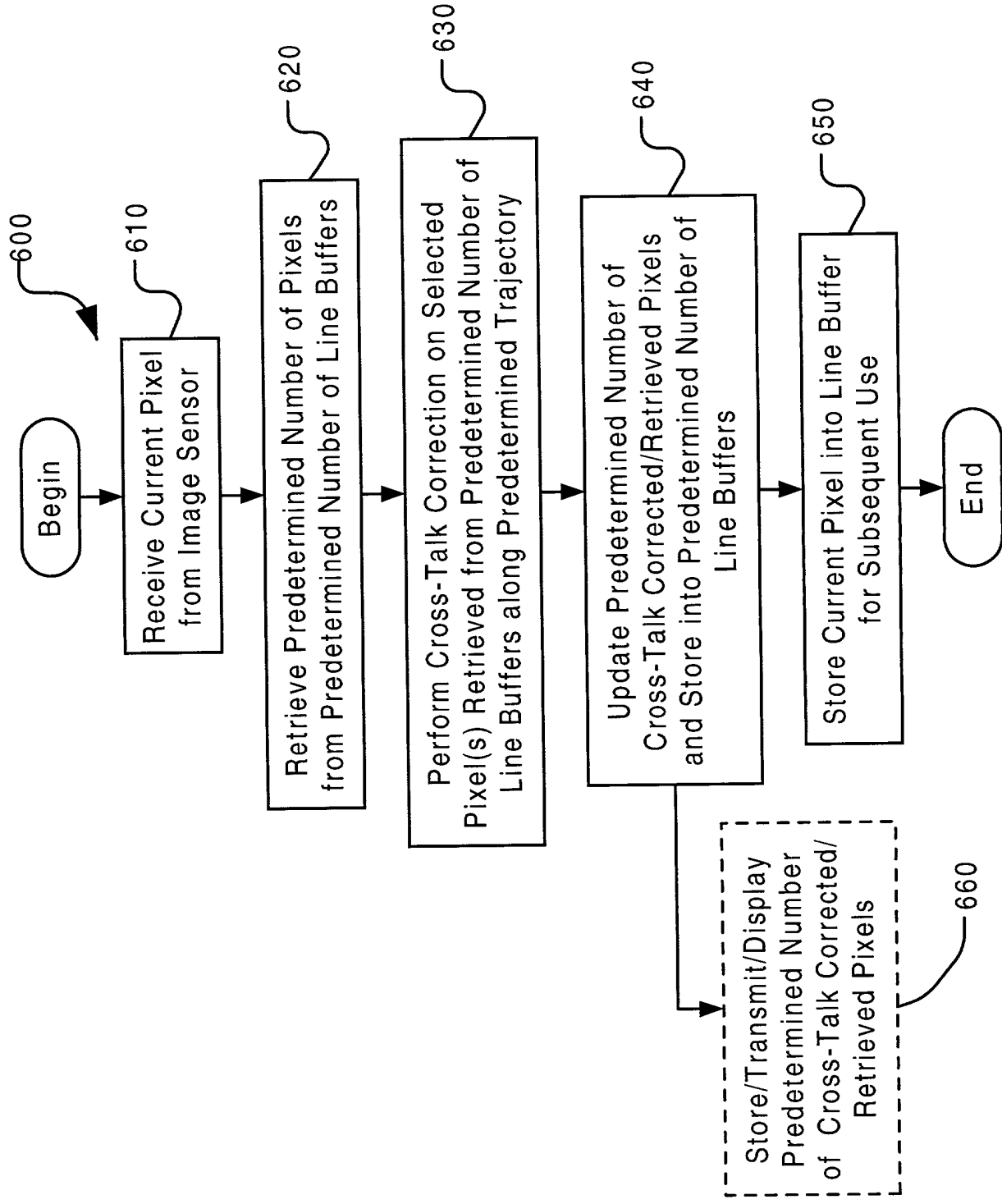


Fig. 6

# DECLARATION FOR PATENT APPLICATION AND POWER OF ATTORNEY

As a below-named inventor, I hereby declare that:

My residence, post office address and citizenship are as stated below next to my name.

I believe I am the original, first, and sole inventor (if only one name is listed below) or an original, first, and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled **DIGITAL IMAGE CROSS TALK CORRECTION**, the specification of which

(check one)      ☒ is attached hereto.

☐ was filed on \_\_\_\_\_ as Application Serial No.: \_\_\_\_\_

I hereby state that I have reviewed and understand the contents of the above-identified specification, including the claims, as amended by any amendment referred to above.

I acknowledge the duty to disclose information which is material to the examination of this application in accordance with Title 37, Code of Federal Regulations, §1.56(a).

I hereby claim foreign priority benefits under Title 35, United States Code, §119(a)-(d) of any foreign application(s) for patent or inventor's certificate listed below and have also identified below any foreign application for patent or inventor's certificate having a filing date before that of the application on which priority is claimed:

## PRIOR FOREIGN APPLICATION(S)

## Priority Claimed

			Yes/No
_____ (Number)	_____ (Country)	_____ (Date Filed)	

I hereby claim the benefit under Title 35, United States Code, §119(e) of any United States provisional application(s) listed below:

_____ (Application Serial No.)	_____ (Filing Date)
-----------------------------------	------------------------

I hereby claim the benefit under Title 35, United States Code, §120 of any United States application or any international application designating the United States under Title 35, United States Code, §365 listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States application in the manner provided by the

first paragraph of Title 35, United States Code, §112, I acknowledge the duty to disclose material information as defined in Title 37, Code of Federal Regulations, §1.56, regarding events which occurred between the filing date of the prior application and the national or PCT international filing date of this application:

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(Number)	(Country)	(Date Filed)
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(Number)	(Country)	(Date Filed)
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I hereby appoint the following attorneys and/or agents to prosecute this application and to transact all business in the Patent and Trademark Office connected therewith:

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I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

**First named inventor:**

Full name Shahriar Najand

Inventor's Signature  Date 9-15-00

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